

**PROGRAMME FOR**  
**DIPLOMA IN ELECTRONICS SEMESTER – VII**  
**EXAMINATION OCTOBER/NOVEMBER - 2013**

**THEORY**

<b>DAY/DATE</b>	<b>SUBJECT</b>	<b>TIME</b>
Monday 21-10-2013	Data Communication and Networking	10.30 a.m. to 12.30 p.m.
Wednesday 23-10-2013	Telematics	10.30 a.m. to 12.30 p.m.
Friday 25-10-2013	Industrial Organization and Management	10.30 a.m. to 12.30 p.m.
Monday 28-10-2013	Documentation Technique – II	10.30 a.m. to 12.30 p.m.

**PRACTICAL**

<b>DAY/DATE</b>	<b>SUBJECT</b>	<b>TIME</b>	<b>Roll No.</b>
Tuesday 29-10-2013	Data Communication and Networking	08.30 a.m. to 10.30 a.m.	12 to 22
		10.30 a.m. to 12.30 p.m.	23 to 32
		12.30 p.m. to 02.30 p.m.	33 to 43
		02.30 p.m. to 04.30 p.m.	01 to 11
Wednesday 30-10-2013	Telematics	08.30 a.m. to 10.30 a.m.	01 to 11
		10.30 a.m. to 12.30 p.m.	12 to 22
		12.30 p.m. to 02.30 p.m.	23 to 32
		02.30 p.m. to 04.30 p.m.	33 to 43
Thursday 31-10-2013	Documentation Technique – II	08.30 a.m. to 04.30 p.m.	01 to 43
Friday 01-11-2013	Electronic Project	08.30 a.m. to 04.30 p.m.	01 to 43

**NAME OF SUPERINTENDENT OF EXAMINATION**

**P.V.POLYTECHNIC**

Mrs. Shubhangini Joshi  
Principal In-Charge  
P. V. Polytechnic  
Juhu, Mumbai-400 049

(Dr. P. N. Mandhare)  
Controller of Examinations

**NOTE: University reserves the right to make changes in the Examination schedule.**